

[illegible]

Not considered - no statement as required by 37 CFR 1.97(e) provided.

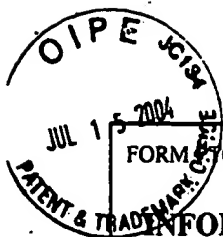


Sheet 2 of 2

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. 114596-03-4000	SERIAL NO. 09/385,394
<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)				APPLICANT John S. Yates, Jr., et al.	
				FILING DATE August 30, 1999	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
		Brad Calder, Peter Feller, Alan Eustace, Value Profiling, Proceedings of 30th International Symposium on Microarchitecture (Micro-30), IEEE, pages 259-269 (December 1-3, 1997)			
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		T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)			
		T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 <sup>th</sup> Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)			
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EXAMINER			DATE CONSIDERED		

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Not Considered - no 37 CFR 1.97(e) statement provided.



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## INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

APPLICANT

John S. Yates, Jr. et al.

FILING DATE

August 30, 1999

GROUP ART UNIT

2183

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	6,363,336	03/26/2002	Banning	703	26	Oct. 13, 1999
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						YES	NO
	WO 99/08188 Kelly	02/18/1999	PCT/US	G06F	11/00		
RUE	WO 99/08191 Kelly	02/18/1999	PCT/US	G06F	12/00		

EXAMINER

Richard Ellis

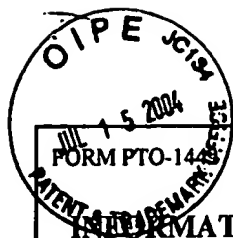
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2/8/2005

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						YES	NO

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		✓	Park et al., Evaluation of Scheduling Techniques on a SPARC-Based VLIW Testbed, Proceedings of the 30th annual ACM/IEEE international symposium on Microarchitecture, pp. 104-113 (1997)
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RLE			Veen: Dataflow Machine Architecture, ACM Computing Surveys vol. 18 no. 4 pp. 765-96 (December 1986)
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RCE				T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 <sup>th</sup> Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)	
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RCE				Dean, ProfileMe: Hardware Support for Instruction-Level Profiling on Out-of-Order Processors," IEEE 1997, pp. 292-302	
RCE				R.E. Hank, et al., Proceeding of the 28 <sup>th</sup> Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)	
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